

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S98	16	subset near5 register near2 cells	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/26 14:30
S99	3	connect\$3 with subset near5 register near2 cells	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/26 14:32
S100	208	connect\$3 with subset near5 cells	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/26 14:32
S101	44	connect\$3 near5 subset adj3 cells	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/26 14:32
S102	4	S101 same shift\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/26 14:32
S103	25	connect\$3 near5 significant near5 register adj2 (bits cells)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/27 16:41
S104	191	connect\$3 near5 register adj2 bits	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/26 16:18
S105	23	connect\$3 near5 register adj2 bits with select\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/26 16:19
S106	285	variable adj delay with shift adj register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/27 10:28
S107	89	variable adj delay adj circuit with shift adj register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/27 10:36
S108	3	S107 with equivalent	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/27 10:28
S109	606	delay adj elements with shift adj register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/27 10:36

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S11 0	14	delay adj elements with shift adj register with switches	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/27 10:36
S11 1	1022	connect\$3 with register adj2 (bits cells)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/27 16:41
S11 2	30	S111 same reduc\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/02/27 16:41